



I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to:

Mail Stop Non-Fee Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

On: September 18, 2004

By: Richard D. Fuerle

Signature:

Date: S∕ex

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: YOSHIYUKI ANDO

Serial No.: 10/707,450

Examiner: Unknown

Group Art Unit: Unknown

Filed:

December 15, 2003

For:

INTEGRATED CIRCUITS HAVING

POST-SILICON ADJUSTMENT CONTROL

The Commissioner of Patents and Trademarks Alexandria, VA 22313

PRELIMINARY AMENDMENT

Introductory Comments

Please amend the above-identified patent application as follows:

09/22/2004 AADDF01 00000011 10707450

01 FC:2201 02 FC:2202 86.00 OP 90.00 OP